

Agenda, UDRC Themed Meeting on Scalable Signal Processing with Bayesian Graphical Models

As part of the UDRC phase III, a first themed meeting on Scalable Signal Processing with Bayesian Graphical Models will be held on Wednesday, February 20th, at the University of Edinburgh, Kings Buildings. This event is intended for academic researchers, industrial partners and Dstl staff to learn about and discuss current trends in scalable Bayesian methods.

The program will consist of a series of talks covering Bayesian sensor fusion, Bayesian networks, approximate message passing and advanced Monte Carlo sampling techniques, followed by a networking session. We will also present a Dstl Challenge competition.

Timings: Wednesday 20th February 2019, Start 9:30am until 4pm

Location: Seminar and Reading Rooms, Centre for Science at Extreme Conditions (CSEC) University of Edinburgh, Erskine Williamson Building, the King's Buildings, Mayfield Road, Edinburgh, EH9 3JZ.

The best way to get into CSEC is through the James Clerk Maxwell Building (JCMB) - the entrance to CSEC is just behind The Magnet cafeteria on level three of JCMB. There will be signs pointing towards the UDRC Meeting from the entrance to the James Maxwell Building to help you. [A map of the campus can be found here](#)

| Timing | Topic | Name |
|---------------|--|---|
| 9:00 – 9:30 | Refreshments | |
| 9:30 – 9:40 | Introductions and Welcome | Yoann Altmann, Heriot-Watt University |
| 9:40 – 10:20 | Convergence Rates for a Class of Monte Carlo Estimators Based on Stein's Method (abstract) | Mark Girolami, Imperial College London |
| 10:20 – 10:50 | Sensor Fusion | David Cormack, Heriot-Watt University; James Hopgood, University of Edinburgh |
| 10:50 – 11:20 | Advanced MCMC | Kostas Zygalakis, Heriot-Watt University |
| 11:20 – 11:50 | Refreshments | |
| 11:50 – 12:20 | Approximate message passing and Expectation Propagation | Yoann Altmann, Heriot-Watt University |
| 12:20 – 12:50 | Scalable opportunistic calibration of fusion networks | Murat Uney, CMRE |
| 12:50 – 13:50 | Lunch and Posters | |
| 13:50 – 14:20 | Scaling signal processing for industry needs | David Greig, Leonardo |
| 14:20 – 14:40 | Dstl challenges | Jordi Barr, Dstl; Gary Heald, Dstl |
| 14:40 – 15:00 | Refreshments | |
| 15:00 – 15:45 | Panel Discussion and Questions | |
| 16:00 | Close | |

