

# Efficient Reconfigurable Mixed Precision $\mathcal{C}_1$ Solver for Compressive Depth Reconstruction

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## Abstract

Rapid reconstruction of depth images from sparsely sampled data is important for many applications in machine perception, including robot or vehicle assistance or autonomy. Approximate computing techniques have been widely adopted to reduce resource consumption and increase efficiency in energy and resource constrained systems, especially targeted at FPGA and solid state implementation. Whereas previous work has focused on approximate, but static, representation of data in LiDAR systems, in this paper we show how the flexibility of an arbitrary precision accelerator with fine-grain tuning allows a better trade-off between algorithmic performance and implementation efficiency. A mixed precision framework of  $\ell_1$  solvers is presented, with compact ADMM and PGD, for the lasso problem, enabling compressive depth reconstruction by varying the precision scaling in single bit granularity during the iterative optimization process. Implementing mixed precision  $\ell_1$  solvers on an FPGA with a pipelined architecture for depth image reconstruction across various sensing scenarios, over **74%** savings in hardware resources and **60%** in power are achieved with only minor reductions in reconstructed depth image quality when compared to single float precision, while over **10%** saving in hardware resources and power is achieved compared to relative consistently reduced precision solutions.

**Keywords** Compressive sensing  $\cdot$  Depth reconstruction  $\cdot$  Mixed precision  $\cdot$  Alternating direction method of multipliers  $\cdot$  Proximal gradient descent  $\cdot$  Field-programmable gate array

# 1 Introduction

Most autonomous systems perceive the surrounding environment via light detection and ranging (LiDAR) [1]. This active sensing technology computes the distance to objects by measuring, at each pixel, the time-of-flight (ToF) between

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<sup>2</sup> Imaging Division, STMicroelectronics R&D Ltd., Tanfield, Inverleith Row, Scotland EH3 5DA Edinburgh, UK emitted and reflected photons. Both the emission and detection of photons consume power, which is of considerable concern in sensor and processing unit design. The current requirements on depth and spatial LiDAR resolution, however, make its power consumption prohibitive and significantly limit its application to resource-constrained platforms, including mobile devices with limited battery supply and physical space, drone scene mapping with SLAM [2], augmented reality [3] and mobile robotics [4].

A promising paradigm to reduce power consumption in resource-constrained devices is approximate computing (AC) [5], which has been widely adopted in signal processing [6], robotics [7] and machine learning [8]. One particular AC technique reduces the arithmetic precision of operations by representing numbers with fewer bits, thereby decreasing the computational cost of memory and logical units. As a result, reduced precision (RP), as it is often known, leads to significant savings in energy consumption [9]. In LiDAR applications, a side benefit of saving energy in data processing is a reduction of thermal noise in the photon detection components, which are often near the data processing unit [10].

The iterative  $\ell_1$  solver Alternating Direction Method of Multipliers (ADMM) was adopted in [11] for parallel compressive sensing (CS) of Lidar based depth image reconstruction, which allowed high 3D imaging frame rates with reduced laser power, memory use, logic cost, and power consumption. Alternatively, the accelerated proximal gradient descent (PGD) algorithm was adopted in [12] for a similar problem. Several other works have investigated the use of AC for convex optimization. In the context of depth reconstruction, Aßmann et al. [13] and Gürel et al. [14] made resource savings by reduction of precision, whereas Wills et al. [15] and Wu et al. [16] have applied approximate PGD methods to Model Predictive Control (MPC). Very recently, Wu et al. [17] deployed reduced precision on convex optimisation using ADMM and PGD for compressive depth reconstruction, not only to reduce power and resource, but also to obtain a faster implementation. However, in previous work, the approximate precision was fixed and pre-determined through the whole execution cycle.

In contrast, mixed precision computation has the potential to enable even further resource savings with overall low arithmetic error for either dense or sparse iterative computations [18]. Automated precision tuning can improve energy efficiency when employing iterative refinement [19]. However, current mixed and auto-tuned precision algorithms are either built for an instruction-driven architecture, or support only floating-point above single precision. This limits both the energy savings [20], and the application of approximation to smaller scale, resource-constrained platforms.

In this work, a new, mixed precision framework is proposed, supporting both fixed and floating point arithmetic. This is applied to 3D LiDAR CS depth reconstruction using a parallel  $\ell_1$  solver of the least absolute shrinkage and selection operator (lasso) problem [13]. Specifically, by applying mixed precision scaling to typical  $\ell_1$  solvers, ADMM and PGD, for compressive depth reconstruction, considerable savings in power, logic and memory resources are achieved.

Our contributions are summarized as follows:

- An ADMM solver using iterative mixed precision implementation.
- A comparative ℓ<sub>1</sub> solver, PGD, with iterative mixed precision.
- A mixed precision framework is created for reconfigurable accelerator generation on an FPGA.
- A comparative study of both  $\ell_1$  solvers between the existing consistent and the new mixed precision framework is evaluated in terms of depth reconstruction quality, implementation cost, and power consumption.

CS depth reconstruction based on convex optimization and the use of precision scaling are introduced in Sect. 2. In



Figure 1 Parallel Depth Reconstruction [13].

Sect. 3, the proposed framework is illustrated. The results are demonstrated in Sect. 4 with a conclusion in Sect. 5.

## 2 Background

#### 2.1 Parallel Depth Reconstruction

Using time-correlated single photon counting (TCSPC), LiDAR systems sample depth information with single photon precision at every pixel that can detect photon events [21–24]. The round trip time of a photon, ToF, and thus the distance travelled by each photon, can be accurately measured. To improve the signal-to-noise ratio, it is common to increase the sampling size by accumulating many photon events in a histogram. Recent advancements in solidstate photon count arrays [10, 25, 26] enable high resolution LiDAR imaging. However, as the number of photon count pixels N, i.e., the resolution of the depth image, increases so does the number of raw histogram measurements.

For high resolution LiDAR, it can therefore be challenging to store and process these raw histogram measurements in real-time.

To address this large data volume, [11] has applied compressive sensing [27, 28] to depth imaging and devised a strategy that processes patches of the depth image independently and in parallel, achieving real-time reconstruction. The framework proposed in [11], named checkerboard compressive depth sensing (CBCS), is illustrated in Fig. 1. It uses a random pattern of illumination (structured light) that is reflected from the scene and acquired by photon pixels in a block-based manner. Each block is reconstructed by solving two lasso problems [29]: one for reconstructing a quantity called the *depthsum*,  $x_Q \in \mathbb{R}^{n_B}$ , where  $n_B$  is the number of pixels in block *B*, and one for reconstructing a quantity called the *photon count intensity*,  $x_I \in \mathbb{R}^{n_B}$ . Specifically, given  $y_Q \in \mathbb{R}^{m_B}$  (resp.  $y_I \in \mathbb{R}^{m_B}$ ) compressive measurements of the depth-sum (resp. photon count intensity),  $x_Q$  and  $x_I$  are recovered by solving

$$\underset{x_{Q}}{\text{minimize}} \quad \frac{1}{2} \| y_{Q} - \overline{A} x_{Q} \|_{2}^{2} + \lambda \| F x_{Q} \|_{1}$$
(1)

minimize 
$$\frac{1}{2} \|y_I - \overline{A}x_I\|_2^2 + \lambda \|Fx_I\|_1$$
, (2)

where  $\overline{A} \in \{0, 1\}^{m_B \times n_B}$  is a known binary matrix that encodes the active pixels in each block for each measurement in  $y_Q$ or  $y_I$ , and  $F \in \mathbb{R}^{n_B \times n_B}$  is a sparsifying matrix, e.g. a DCT matrix, that we assume is invertible.  $\lambda > 0$  is a regularisation parameter, and  $\|\cdot\|_2$  and  $\|\cdot\|_1$  are, respectively, the  $\ell_2$ - and  $\ell_1$ -norms. After recovering these quantities, the final depth image at block *B* is computed by dividing  $x_Q$  by  $x_I$  pointwise:  $x_D = x_Q \cdot / x_I \in \mathbb{R}^{n_B}$ . The work in [11] also proposed a post-processing strategy to remove blocking effects. The problems in (1) are ubiquitous in signal processing and can be solved efficiently via ADMM [30] and PGD [31].

#### 2.2 Mixed Precision and Tuning

Mixed precision combines the accelerated and less resource intensive processing of lower precision with the greater accuracy of higher precision arithmetic. It is normally deployed for iterative refinement using floating point data formats [32, 33], for example using three levels of precision [34]. Customized precision tuning instruments have been employed based on specific input sets [19]. Such precision scaling has gained increasing interest for computation-intensive applications, such as deep learning, saving computation and data storage [35] using both *floating point* 

$$-1^{\rm S} \times {\rm M} \times 2^{\rm E-127},$$
 (3)

where S is the sign, M the mantissa, and E the exponent; and *fixed point* 

$$-1^{8} \times (\mathrm{I} + \mathrm{F}), \tag{4}$$

where S is the sign, I the integer value as sum of  $2^b$ , and F the fraction value as the sum of  $2^{-b}$  from each bit position *b* depending on the 2's complement format [36].

Different arithmetic types yield different dynamic ranges. Floating point with nonlinear binary representation uses less bits, while fixed point enables simpler binary operations but requires more bits. Hence, the various precisions in both floating and fixed point affect not only the algorithm's performance, but also the embedded implementation.

By adjusting the combination of mixed precision arithmetic at compile-time, static precision tuning is performed once during the system design phase [34]. Such a tuning process can be considered as part of wider hardware/software co-design approaches, profiling the application data and code to compare with empirical measurement before running the system. On the other hand, dynamic precision tuning is invoked multiple times during the execution of the algorithm at run-time, which can incur some overhead [19].

We develop a framework for CS depth reconstruction, using ADMM and PGD to solve lasso. Its key aspect is the mixed precision design of both the arithmetic data type and the binary bit width, appropriate to the performance requirements of the sensing environment. This provides an alternative to our earlier work [17], which used constant precision, and results in a significantly more energy efficient implementation.

## **3 Mixed Precision Framework**

Optimization-based algorithms for lasso are iterative, with the same sequence of steps occurring at each iteration. Traditionally, all computations are performed with the same precision. Here, instead, we consider the case in which different iterations use different precision. We focus firstly on ADMM, secondly on PGD. Then, we present the design flow of our mixed precision accelerator.

## 3.1 Mixed Precision ADMM

We consider the following reformulation of the problem in (1 and 2):

$$\begin{array}{l} \underset{x,z}{\text{minimize }} \frac{1}{2} \|y - Ax\|_2^2 + \lambda_{\text{ADMM}} \|z\|_1 \\ \text{subject to } x - z = 0 \,, \end{array} \tag{5}$$

where we define  $A := \overline{A}F^{-1} \in \mathbb{R}^{m \times n}$ . Each iteration of ADMM [30] applied to (5) consists of

$$x^{k+1} = \left(A^{\mathsf{T}}A + \rho I_n\right)^{-1} \left[A^{\mathsf{T}}y + \rho(z^k - u^k)\right]$$
(6)

$$z^{k+1} = S_{\lambda_{\text{ADMM}/\rho}} \left( u^k + x^{k+1} \right) \tag{7}$$

$$u^{k+1} = u^k + x^{k+1} - z^{k+1}, (8)$$

where  $\rho > 0$  is the augmented Lagrangian parameter,  $I_n \in \mathbb{R}^{n \times n}$  the identity matrix,  $u \in \mathbb{R}^n$  a dual variable, and  $S_{\lambda}(\cdot)$  the soft-thresholding operator applied componentwise: for  $v \in \mathbb{R}$ ,  $S_{\lambda}(v) = v - \lambda$  when  $v \ge \lambda$ ,  $S_{\lambda}(v) = v + \lambda$ when  $v < -\lambda$ , and  $S_{\lambda}(v) = 0$  otherwise. If the parameter  $\rho$  is constant throughout the iterations, the matrix  $A^{\top}A + \rho I_n$  and its inverse can be precomputed. This can be done efficiently via the matrix inverse lemma [37],

$$\left(A^{\mathsf{T}}A + \rho I_{n}\right)^{-1} = \frac{1}{\rho}I_{n} - \frac{1}{\rho^{2}}A^{\mathsf{T}}\left(I_{m} + \frac{1}{\rho}AA^{\mathsf{T}}\right)^{-1}A$$
(9)

and by computing the inverse of the  $m \times m$  matrix in (9) via its Cholesky decomposition,  $I_m + (1/\rho)AA^{\top} = LL^{\top}$ , where  $L \in \mathbb{R}^{m \times m}$  is lower triangular. The quantity  $g := A^{\top}y$  can also be pre-computed. In our context, such pre-computations have the additional benefit of avoiding unnecessary accuracy loss when using mixed precision during the iterations. The resulting algorithm (with fixed precision) is shown in Algorithm 1, from which we can see that each iteration requires  $O(n + m^2)$  arithmetic operations. Step 4 of Algorithm 1 differs from (7) in that it uses over-relaxation, parameterized by  $0 < \alpha < 2$ , which can improve convergence [30].

<b>Algorithm 1</b> ADMM for lasso in Eq. $(5)$
<b>Input:</b> A, L, $\kappa = \lambda_{\text{ADMM}} / \rho$ , $g = A^{\top} y$ , $k_{\text{max}}$ , $\alpha$
Initialization: $z^0 = u^0 = 0_n$
1: for $k = 0, 1,, k_{\max}$ do
2: $q^{k+1} = g + \rho(z^k - u^k)$
3: $x^{k+1} = \frac{1}{\rho}q^{k+1} - \frac{1}{\rho^2}A^{\top}(L^{\top})^{-1}L^{-1}Aq^k$
4: $z^{k+1} = S_{\kappa}(u^k + \alpha x^{k+1} + (1-\alpha)z^k)$
5: $u^{k+1} = u^k + x^{k+1} - z^{k+1}$
6: end for
7: return $x^{k_{\max}+1}$

Algorithm 2 Mixed precision ADMM for lasso
<b>Input:</b> $g = A^{\top}y, H = A^{\top}L^{\top - 1}L^{-1}A, k_{\max}$ <b>Initialization:</b> $z^{0} = u^{0} = 0_{n}, l_{0} = 0$ 1: for $k = 0, 1, \dots, k_{\max}$ do
2: $(g_c, H_c, z_c^k, u_c^k) = \operatorname{cast}_c^{(l_k)}(g, H, z^k, u^k)$
3: $(z^{k+1}, u^{k+1}) = * \text{mixiter}^{(l_k)}(g_c, H_c, z_c^k, u_c^k)$
4: $l_k \leftarrow l_k + 1$
5: end for
6: return $z^{k_{\max}+1}$

Algorithm 2 implements Algorithm 1 using mixed precision. It takes as input the constant vector  $g = A^{T}y$  and matrix  $H = A^{T}L^{T^{-1}}L^{-1}A$ , and the maximum number of iterations  $k_{max}$ . At each iteration k, the algorithm recasts the constants g and H and the variables  $z^{k}$  and  $u^{k}$  to the precision required at the current iteration. In software such a function is implemented conceptually as a data type cast, while in hardware it is implemented through data path trimming, where the extra data path are unconnected between mismatched bit-width.

Algorithm 3  $(\hat{z}, \hat{u}) = *\text{mixiter}(g, H, z, u)$ Input: g, H, z, uInitialization: constants  $\alpha, \hat{\alpha}, \rho, \hat{\rho}, \kappa$ 1:  $q = g + \rho \cdot (z - u)$ 2:  $x = \alpha \cdot (\hat{\rho} \cdot q - H \cdot q) + \hat{\alpha} \cdot z$ 3:  $z = \max\{0, (x + u) - \kappa\} - \max\{0, -(x + u) - \kappa\}$ 4: u = u + x - z5: return z, u

These variables are then used by the mixed precision function \* mixiter, described in Algorithm 3, which performs the same steps as each iteration of Algorithm 1, but with precision specified by  $l_k$ . This variable represents the index to a set of static functions, each of which is written for a predefined precision, e.g., floating point with 32 bits or fixed point with 30 bits. Each of these functions uses the precomputed constants  $\alpha$ ,  $\hat{\alpha} := 1 - \alpha$ ,  $\rho, \hat{\rho} := 1/\rho$ , and  $\kappa := \lambda_{ADMM}/\rho$ . The initial index  $l_0$  is found based on the maximum *e* representing the precision loss given a certain threshold. This process is described in Sect. 3.2. During the iterations,  $l_{k}$  is modified to meet the demanding finer accuracy of the algorithm, increasing its precision and changing the elementary bit width gradually. We fixed the number of iterations to  $k_{\text{max}} = 5$ , determined empirically, as no further improvement on reconstruction of depth has been observed beyond this point [17]. In the particular case of parallel computation using small blocks,  $4 \times 4$  pixels, ADMM converges in very few iterations.

## 3.2 Mixed Precision PGD

X

We now consider the application of proximal gradient descent (PGD) to (1). Each of those problems can be written as

minimize 
$$\frac{1}{2} \|y - Ax\|_2^2 + \lambda_{\text{PGD}} \|x\|_1$$
, (10)

where  $A := \overline{A}F^{-1} \in \mathbb{R}^{m \times n}$ . Defining the convex differentiable function  $g(x) := (1/2)||y - Ax||_2^2$ , the convex function  $h(x) = \lambda_{\text{PGD}}||x||_1$ , and their sum f(x) = g(x) + h(x), PGD applied to (10) yields the iterative shrinkage thresholding (ISTA) algorithm [31, 38]:

$$E^{k+1} = \operatorname{prox}_{\alpha_k h} (x^k - \alpha_k \nabla g(x^k))$$
  
=  $S_{\alpha_k \lambda_{PGD}} \left( x^k - \alpha_k A^{\mathsf{T}} A x^k + \alpha_k A^{\mathsf{T}} y \right),$  (11)

where  $\alpha_k \ge 0$  is the step size at iteration k, and  $\operatorname{prox}_{\alpha_k h}(\cdot)$  the proximal operator of  $\alpha_k h$  which, in this case, is the softthresholding operator  $S_{\alpha_k \lambda_{\text{PGD}}}$ , defined in Sect. 3.1. PGD converges whenever  $0 < \alpha_k \le 1/L$ , where L is the Lipschitz constant of the gradient of g, i.e.,  $\nabla g(x) = A^{\mathsf{T}}A$ . That is, L can be found as the maximum eigenvalue of  $A^T A$ . We use a constant stepsize:  $\alpha_k = \alpha = 1/L$ .

Algorithm 4 PGD for lasso in Eq.(10)
Input: $A, g = A^{\top}y, \lambda_{PGD}, k_{max}$ Initialization: $x^0 = 0_n, \alpha = 1/\max \operatorname{eig}(A^{\top}A), g_{\alpha} = \alpha g, W = \alpha A^{\top}A, \kappa = \alpha \lambda_{PGD}$
1: for $k = 0, 1,, k_{\max}$ do 2: $v^{k+1} = Wx^k - g_{\alpha}$ 3: $x^{k+1} = S_{\kappa}(x^k - v^{k+1})$ 4: end for 5: return $x^{k_{\max}+1}$

The general algorithm is described in Algorithm 4. It takes as input the matrix *A*, the vector  $g := A^{\mathsf{T}}y$ , the regularizer constant  $\lambda_{\mathsf{PGD}}$ , and the maximum number of iterations  $k_{\max}$ . After computing the stepsize  $\alpha$ , it precomputes the vector  $g_{\alpha} := \alpha g$ , the matrix  $W := \alpha A^{\mathsf{T}}A$ , and the threshold  $\kappa := \alpha \lambda_{\mathsf{PGD}}$ . Steps 2 and 3 then implement exactly (11).

Algorithm 5 Mixed precision PGD for las	sso
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Input:  $A, g = A^{\top}y, \lambda_{PGD}, k_{max}$ Initialization:  $x^0 = 0_n, \alpha = 1/\max \operatorname{eig}(A^{\top}A), g_{\alpha} = \alpha g, W = \alpha A^{\top}A, \kappa = \alpha \lambda_{PGD}, l_0 = 0$ 1: for  $k = 0, 1, \dots, k_{max}$  do 2:  $(W_c, (g_{\alpha})_c, x_c^k) = \operatorname{cast}_c^{(l_k)}(W, g_{\alpha}, x^k))$ 3:  $x^{k+1} = *\operatorname{mixiter}^{(l_k)}(W_c, (g_{\alpha})_c, x_c^k))$ 4:  $l_k = l_k + 1$ 5: end for 6: return  $x^{k_{max}+1}$ 

<b>Algorithm 6</b> $\widehat{x} = *\text{mixiter}(W, g, x)$
<b>Input:</b> $W, g, x$ <b>Initialization:</b> constant $\kappa$
1: $v = Wx - g$ 2: $x = \max\{0, (x - v) - \kappa\} - \max\{0, -(x - v) - \kappa\}$ 3: return $x$

Algorithm 5 implements Algorithm 4 using mixed precision. Both algorithms have the same inputs and initialization, except that Algorithm 5 now also initializes the function pointer with index  $l_k$ , which specifies a static function \* mixiter<sup> $l_k$ </sup>(·) that performs the main computations of PGD with a given precision. The order of these functions, which are accessed via  $l_k$ , determines the sequence of precisions, maintained through combined search of various bit widths based on the precision loss of the precomputed parameters and a post-search fine tuning. As in mixed precision ADMM, all the relevant variables in Algorithm 5 are cast to the required precision and then used in the \* mixiter function indexed by  $l_k$ . cast<sub>c</sub> only demonstrates the arithmetic precision transformation at the software level, and is not an overhead in hardware design at the appropriate precision. In this work, we only consider mixed precision within the same arithmetic type. As in mixed precision ADMM, we set the maximum number of iterations of PGD to  $k_{\text{max}} = 5$ .

## 3.3 Mixed Precision Accelerator

We developed the semi-automated framework illustrated in Fig. 2 based on the Matlab and Xilinx Vivado toolsets. It quickly prototypes an approximate accelerator with mixedprecision arithmetic on reconfigurable platforms. To support mixed precision design, we created an approximate generic linear algebra library calling third-party arithmetic types, which are based firstly on a customized floating-point library FloatX [32] and secondly on the Xilinx fixed-point library [39]. The library was established with C++ templates in the header only, which allows later specific arithmetic type allocation. Its major features are:

- General (non-symmetric) real value operations.
- Arbitrary bit width floating- and fixed-point arithmetic.
- Basic vector/matrix algebra arithmetic (addition, subtraction, multiplication, division, inversion).
- Triangular factorization (LU, and Cholesky decomposition).
- Orthogonal factorization (QR decomposition).

Based on the approximate linear algebra library, a userdefined kernel, which indicates the specific portion of signal processing application to be the target for approximation, is considered as input in Fig. 2. By compiling the input source, a list of various iterative functions are created using different arithmetic precisions, as expressed in both Algorithms 3 and 6. The source compiler is developed based on the Matlab MEX compiler API where the Design Space Exploration (DSE) with precision adaptation is evaluated based on the input pre-computed data.

The quantization effect of the data from single-precision floating point (32 bits, called full precision in the later context of this paper) to reduced precision is considered as the criterion for precision scaling. Specifically, the normalized absolute difference is used for floating point (FP) arithmetic while the absolute difference is adopted for fixed point



Figure 2 Mixed Precision Design Flow.

(FXP) arithmetic due to the different functional relationships between the binary representations and decimal values. The most representative values, the maximum and minimum of those pre-computed parameters, are picked for the numerical analysis.

To measure the effects of different FP quantizations, we use the normalized absolute difference

$$\epsilon = \frac{\|v - \operatorname{cast}_c(v)\|_1}{\|v\|_1},\tag{12}$$

where  $v \in \mathbb{R}^n$  is a vector and  $\|\cdot\|_1$  the  $\ell_1$ -norm. To measure the effects of different FXP quantizations, we simply use the absolute difference

$$\epsilon = \|v - \operatorname{cast}_c(v)\|_1. \tag{13}$$

We guarantee that in either (12) or (13), we always have  $\epsilon \leq \omega = 5 \times 10^{-3}$ . Different arithmetic types will yield different such  $\epsilon$ . Using exhaustive search, we select the bit width of different parts in the binary arithmetic format for either FP or FXP as the smallest that satisfies  $\epsilon \leq \omega$ . The representation with that bit width is then indexed by  $l_0$  in Algorithms 2 and 5.

The kernel with chosen precision is adopted for both the integer and fraction part representations to achieve a relatively close reconstruction performance to full precision, where heuristic fine tuning is performed at the DSE stage. The accelerator is generated with a Hardware Description Language (HDL) by calling Xilinx High-Level Synthesis (HLS) tools within the Matlab Framework. With combined algorithm evaluation and hardware implementation in a loop, it can quickly prototype mixed precision design on reconfigurable platforms.

## 4 Evaluation

Both  $\ell_1$  solvers, ADMM and PGD, using either floating (FP) or fixed-point (FXP) arithmetic, are illustrated and evaluated for compressive depth reconstruction on two synthetic and two real underwater sensing datasets. Reconstruction of the depth image using *d*Sparse [13] (a non-compressive approach with oversampling) with full, single floating point (FP 32 bits) precision is adopted as a benchmark for comparison.

The FP 32 ADMM or PGD solutions are considered as the baseline, while the identified highest accuracy in the pre-scheduled list is considered as the most suitable reduced precision solution. Both are adopted for comparisons of recovered image fidelity as well as implemented hardware clock speed, resource usage and power consumed.



**Figure 3** Short range dataset [40]: recovered depth images using **a** *d*Sparse, consistent FP 32 bit); and ADMM solver with **b** consistent FP 32 bit; **c** consistent FP 18 bit; **d** consistent FP 22 bit; **e** mixed pre-

cision from FP 18 to 22 bit for each iteration; **f** consistent FXP 20 bit; **g** consistent FXP 24 bit; **h** mixed precision from FXP 20 to 24 bit for each iteration.

### 4.1 Depth Reconstruction Accuracy

The Peak Signal-to-Noise Ratio (PSNR) and Structural Similarity Index Measure (SSIM) [41] are both considered as quality metrics, showing the fidelity of reconstruction and the perceived quality based on the pixel-wise absolute difference for normalised similarity scaling. Four different sensing environments are adopted to evaluate the quality of depth reconstruction with mixed precision: short range [40] and long range [42] synthetic benchmarks, and real LiDAR data for two very short range, underwater scenes [43]. In the short range scenario, the detailed mixed precision phenomenon is evaluated firstly against the reconstructed depth image quality for both PSNR and SSIM. Then mixed precision is applied to the other three scenarios, where the corresponding depth reconstruction accuracy is also evaluated.

#### • Scene 1: short range

By using the design flow for different scenes at different stand-off distances and dynamic ranges, mixed FP from 18 to 22 bits, and FXP from 20 to 24 bits are employed for the short range synthetic scene with distances ranging from 1.8 to 3.4 meters. Figures 3 and 4 illustrate the depth reconstruction accuracy against various consistent and mixed precision for both  $\ell_1$  solvers, ADMM and PGD. The ground truth is set as *d*Sparse using single

precision floating point with 32 bits, where its PSNR and SSIM are infinity and 1 since the comparison is to itself.

The recovered depth images using the ADMM solver with FP arithmetic are shown in Fig. 3b-e. The PSNR and SSIM are 28 dB and 0.75 respectively for the baseline FP 32 bits (Fig. 3b), while, comparatively, the worst (FP 18) and best (FP22) quality of the pre-scheduled consistent FP precision are illustrated in Fig. 3c, with 0.15 loss in SSIM, and Fig. 3d, with similar PSNR and SSIM. Increasing the bit-width is shown by  $\rightarrow$ , the mixed FP precision (FP 18 $\rightarrow$ 22 bits) in Fig. 3e shows only 0.022 dB loss in PSNR and 0.009 loss in SSIM. It has even slight better PSNR than the consistent FP 22 solution. Corresponding depth images recovered by using the FXP ADMM solver are shown in Fig. 3f and g. Similarly, the worst case (FXP 20) in Fig. 3f is degraded in both PSNR and SSIM while the best case (FXP 24) in Fig. 3h has even better PSNR and SSIM than the baseline in Fig. 3b. Using mixed precision FXP  $20 \rightarrow 24$  bits, there is a PSNR loss of 0.19 dB and a 0.004 loss in SSIM.

Similarly, Fig. 4b–e illustrate the recovered depth image using the PGD solver with FP, while Fig. 4f–h are those using FXP. The baseline in Fig. 4b achieves the same SSIM as the FP baseline with slightly better PSNR. The worst case (FP 17) of pre-scheduled, consistent FP in Fig. 4c degrades in both PSNR and SSIM, while the



**Figure 4** Short range dataset [40]: recovered depth images using **a** *d*Sparse with single precision, FP 32 bit; and PGD solver with **b** consistent FP 32 bit; **c** consistent FP 17 bit; **d** consistent FP 21 bit; **e** 

mixed precision from FP 17 to 21 bit for each iteration; **f** consistent FXP 18 bit; **g** consistent FXP 22 bit; **h** using mixed precision from FXP 18 to 22 bit for each iteration.





best case in Fig. 4d is almost the same as the baseline. The worst case (FXP 18) of pre-scheduled, consistent FXP has even better PSNR than the baseline but is much worse in SSIM. Both mixed FP and FXP precision show minor loss in both PSNR and SSIM.

To further illustrate the use of mixed precision, PSNR and SSIM are plotted against bit width for both ADMM and PGD using FP and FXP representations in Figs. 5–8. The consistent precision, shown as a red line, refers to the use of the same precision over all iterative computations, and is compared to the use of mixed precision shown as blue line.

As shown in Figs. 5 and 6, for the ADMM solver, the PSNR and SSIM values using mixed precision match the highest, consistent precision in either FP or FXP. An exception is the PSNR of mixed FXP precision in Fig. 5b, which is gradually degraded as the bit width decreases.

Similar trends exist for the PGD solver as shown in Figs. 7 and 8, where all the PSNR and SSIM values for mixed FXP precision match those of the highest consistent precision in either FP or FXP, degrading with decreasing bit width.

Summarising, from Figs. 5–8 we observe that the mixed precision does achieve similar fidelity of the reconstructed depth image when compared to the highest pre-scheduled precision, and to the baseline single floating point precision. However, there are differences between the FP and FXP representations during the transition from low to high precision in the pre-scheduled mixed precision list. The FP representation has a smooth transition for both PSNR and SSIM. Due to the computational uncertainty of truncating errors, the transition of PSNR is slightly bumpy for the FXP representation, but the transition of SSIM is a smooth function of the FP representation.

• Scene 2: long range

Using both ADMM and PGDs, mixed FP from 16 to 20 bits, and FXP from 30 to 34 bits are adopted for the long range, synthetic scene with distances ranging from 0 to 100 meters. Figure 9 illustrates the comparisons of depth reconstruction. The baselines of the reconstructed depth image using ADMM and PGD solvers are illustrated in Fig. 9b and e. The PSNR using the ADMM solver is slightly better than the PGD solver by 0.1 dB, while the SSIM using the ADMM solver is slightly worse by 0.07.



Figure 6 ADMM: SSIM v.s. Bitwidth (Short Range Dataset).

Figure 7 PGD: PSNR v.s.



With mixed precision using the ADMM solver, there is a 0.2 dB loss of PSNR for FP from 18 to 22 bits and 0.021 dB loss of PSNR for FXP from 28 to 32 bits. Using the PGD solver, the mixed FP precision from 18 to 22 bits shows a slightly better PSNR than the baseline, and than mixed FXP precision from 30 to 34 bits. The SSIM with both FP and FXP mixed precision are both slightly worse than the baseline by about 0.006.

Summarising, evaluation of the use of mixed precision in the long range, as in the short range synthetic scenario, confirms the comparable fidelity of reconstructed depth image to both the baseline and the consistent precision solutions. Further evaluation on real data is now considered.

Scene 3: underwater A

This scene has distances ranging from 27 to 39 centimeters. Mixed FP from 18 to 22 bits using both the ADMM and PGD solvers in Fig. 10c and f, FXP from 28 to 32 bits using the ADMM solver in Fig. 10d and FXP from 30 to 34 bits using the PGD solvers in Fig. 10g are adopted.

There is a slight PSNR degradation of about 0.2 dB using ADMM with mixed FP precision, and an almost identical PSNR with mixed FXP precision. SSIM is degraded by around 0.01 for both mixed FP and FXP precision using ADMM. For mixed precision using the PGD solver, the PSNR with both mixed FP and FXP precision is about 0.1 dB better than the baseline, while the SSIM is slightly degraded by 0.005.

Scene 4: underwater B

This scene has distances ranging from 31.4 to 31.8 centimeters. Mixed FP from 23 to 27 bits using ADMM in Fig. 11c, FP from 19 to 23 bits using PGD in Fig. 11f, FXP from 32 to 36 bits using the ADMM solver in Fig. 11d and FXP from 30 to 34 bits using the PGD solver are illustrated in Fig. 11g. For FP and FXP mixed precision using both the ADMM and PGD solvers, the PSNR amd SSIM are always maintained at a similar level to the baseline FP 32 bits.

Hence, from our experiments on diverse synthetic and real data, illustrated in Figs. 3-11, we can make the following observations.

- i The use of reduced precision in both FP and FXP representations leads to depth reconstruction that has considerable fidelity to the full precision solutions.
- ii The use of mixed precision, introduced in this paper, also achieves minor losses in PSNR and SSIM when







**Figure 9** Long range dataset [42]: recovered depth images using **a** *d*Sparse with consistent FP 32 bit; **b** ADMM, consistent FP 32 bit; **c** ADMM, mixed FP 16 to 20 bits; **d** ADMM, mixed FXP 30 to 34 bit;

e PGD, consistent FP 32 bit; f PGD, mixed FP 16 to 20 bits; g PGD, mixed FXP 30 to 34 bit.



Figure 10 Underwater dataset A [44]: recovered depth images using a *d*Sparse with consistent FP 32 bit; b ADMM, consistent FP 32 bit; c ADMM, mixed FP 18 to 22 bits; d ADMM, mixed FXP 28 to 32

bit;  $\mathbf{e}$  PGD, consistent FP 32 bit;  $\mathbf{f}$  PGD, mixed FP 18 to 22 bits;  $\mathbf{g}$  PGD, mixed FXP 30 to 34 bit.



Figure 11 Underwater dataset B [44]: recovered depth images using a *d*Sparse with consistent FP 32 bit; b ADMM, consistent FP 32 bit; c ADMM, mixed FP 23 to 27 bits; d ADMM, mixed FXP 32 to 36

bit; e PGD, consistent FP 32 bit; f PGD, mixed FP 19 to 23 bits; g PGD, mixed FXP 32 to 36 bit.

compared to even the highest, fixed precision representations, and is significantly better than the lower precision solutions. This shows a path to even greater resource savings.

- iii The use of higher precision in the later iterations of  $\ell_1$  solvers makes a more significant contribution to the quality of optimization outcome, while the use of lower precision in the earlier iterations is either tolerable or can be well compensated by the later iteration with higher precision.
- iv For different mixed precision  $\ell_1$  solver solutions, PGD incorporates lower precision in general than ADMM to achieve similar fidelity in general, since PGD has simpler computational operations with less opportunity of precision loss.
- v Given that we can achieve similar fidelity with mixed precision, we anticipate that significant cost and power savings are achievable with a mixed strategy, when compared computation using fixed precision, whether full or even reduced through all iterations.

Having shown that the use of mixed precision is possible without significant degradation in quality of reconstruction, we now proceed to show how the use of mixed precision can result in further savings in hardware utilization and power consumption in the next section.

## **4.2** $\ell_1$ Solver Accelerators

We have designed and implemented fully pipelined architectures using single and mixed precision with FP and FXP arithmetic on a Xilinx Ultrascale+ ZCU106 architecture using Vivado 2019.1 to fast prototype our design without exhaustive hardware optimization.

Following evaluation of algorithmic performance, the corresponding pipelined architecture has been implemented for either consistent or mixed precision ADMM and PGD to solve the twin lasso optimization problem in Eq. 1, where



Figure 12 An Example of PGD Architecture.

each iteration corresponds to a pipeline stage with either the same or an allocated, different precision. This strategy can be extended to more iterations, considering mixed precision between batched sub-iterations.

An example of the dataflow architecture of implemented accelerator using PGD  $\ell_1$  solver with mixed floating point precision is illustrated in Fig. 12. The *MVMul* performs the matrix-vector multiplication, where other *VS* \* blocks are vector-scalar addition, subtraction, multiplication and comparison. *VSub* implements the vector subtraction.

The datapath of exponent and mantissa are separated between iterative mixed precision, where the lower bit-width is connected to corresponding part of later higher bit-width pipelines. The outcomes from the twin  $\ell_1$  solver,  $x_Q$  and  $x_I$ , adopt a vector element-wise division to obtain the final depth pixels  $x_D$ .

Resource utilization is derived from the Xilinx tools using the place and route implementation, while the power is estimated using the Xilinx Power Estimator (XPE). Table 1 presents the resource and power comparisons of both consistent and mixed precision using the ADMM and PGD solvers for all scenarios, which also lists the performance of system reaction latency and processing speed in pixel/s given fixed number of pixel in a depth image. The mixed precision values correspond to the bit width variation chosen according to the depth scaling for each of the scenes while the

Table 1 Resource and quality metrics for consistent and mixed precision. For custom floating point, the bit width = 1+E+M and for custom fixed point, the bit width = 1+I+F.

			Resources			Precision and Performance				
		Scene	LUT	DSP	Dynamic	Bit Width	Frequency	Latency	Throughput	Energy
	Arithmetic	Index		(DSP48E2)	Power (W)	(bits)	(MHz)	(ms)	Pixel/s	(µJ/Pixel)
<i>t</i> ADMM	FP32	1,2,3,4	20815	110	0.774	(1,8,23)	439	0.26	3.07e5	2.52
p = 8	FP27	4	21885	50	0.660	(1,6,20)	407	0.26	3.07e5	2.14
<i>n</i> = 16	FP23→27	4	20405	40	0.626	(1,6,16→20)	394	0.24	3.33e5	1.88
	FP22	1,3	18455	25	0.577	(1,6,15)	430	0.27	2.96e5	1.95
	FP18→22	1,3	16192	25	0.535	(1,6,12→15)	430	0.25	3.20e5	1.67
	FP20	2	15680	25	0.541	(1,6,13)	472	0.25	3.20e5	1.69
	FP16→20	2	11092	25	0.494	(1,6,9→13)	473	0.23	3.47e5	1.42
	FXP36	4	9990	60	0.381	(1,23,12)	418	0.09	8.88e5	0.43
	FXP32→36	4	9454	60	0.374	(1,23,8→12)	417	0.09	8.88e5	0.42
	FXP34	2	9375	60	0.375	(1,26,7)	401	0.09	8.88e5	0.42
	FXP30→34	2	8902	57	0.367	(1,25,4→8)	401	0.09	8.88e5	0.41
	FXP32	3	8950	60	0.369	(1,21,10)	406	0.08	1.00e6	0.37
	FXP28→32	3	8461	54	0.357	(1,21,6→10)	406	0.08	1.00e6	0.36
	FXP24	1	6775	30	0.316	(1,13,10)	415	0.07	1.14e6	0.28
	FXP20→24	1	6807	21	0.307	(1,13,6→10)	403	0.07	1.14e6	0.27
PGD	FP32	1,2,3,4	16020	95	0.641	(1,8,23)	415	0.10	8.00e5	0.81
p = 8	FP23	4	14845	25	0.537	(1,6,16)	421	0.08	1.00e6	0.54
<i>n</i> = 16	FP19→23	4	13116	25	0.510	(1,6,12→16)	419	0.08	1.00e6	0.51
	FP22	3	14115	25	0.524	(1,6,15)	469	0.09	8.88e5	0.59
	FP18→22	3	12368	25	0.497	(1,6,11→15)	453	0.09	8.88e5	0.56
	FP21	1	13070	25	0.506	(1,6,14)	453	0.09	8.88e5	0.57
	FP17→21	1	11663	25	0.484	(1,6,10→14)	453	0.08	1.00e6	0.48
	FP20	2	12020	25	0.496	(1,6,13)	473	0.09	8.88e5	0.56
	FP16→20	2	11092	25	0.473	(1,6,9→13)	469	0.08	1.00e6	0.47
	FXP36	4	7520	80	0.380	(1,23,12)	417	0.01	8.00e6	0.048
	FXP32→36	4	6162	80	0.368	(1,23,8→12)	412	0.01	8.00e6	0.045
	FXP34	3	5860	80	0.366	(1,21,12)	407	0.01	8.00e6	0.045
	FXP30→34	3	5537	80	0.362	(1,21,8→12)	408	0.01	8.00e6	0.044
	FXP32	2	5425	80	0.361	(1,27,4)	428	0.01	8.00e6	0.045
	FXP28→32	2	5060	68	0.329	(1,23→27,4)	400	0.01	8.00e6	0.04
	FXP22	1	5330	20	0.300	(1,12,9)	378	0.008	1.00e7	0.03
	FXP18→22	1	4151	20	0.283	(1,12,5→9)	359	0.008	1.00e7	0.028

consistent precision cases are those of the highest bit width in the mixed precision combination.

The resource utilization of the Look-Up-Table (LUT) and Digital Signal Processor (DSP) units are reduced by up to 67% and 80% respectively for the ADMM solver using mixed 20 to 24 bit-width fixed-point precision, and by up to 74% and 78% for the PGD solver using mixed 18 to 22 bit-width fixed-point precision. The related power consumption is significantly reduced by up to 60% and 55% for ADMM and PGD solver respectively. Though some mixed fixed-point implementation consume more DSPs than mixed floatingpoint, they are all less than using single precision floating point.

Hence, by adopting mixed precision with either FP or FXP, there are significant hardware resource and power savings when compared to single float precision. Given

the similar algorithmic performance between the ADMM and PGD solvers, the outcomes for PGD are better than ADMM in all aspects, both resource utilization and power consumption. Due to the simplicity of the computational operations, the implemented PGD solver gains not only in hardware cost but also in throughput in pixels per second, and in quantified efficiency measured as energy consumption per pixel.

For the hardware implementations, the quality of depth reconstruction is quantified against the consumed resource and power in Figs. 13 and 14 for both the ADMM and PGD solvers. Only SSIM is adopted here as it is more sensitive to individual range errors, though PSNR should have similar trend.

Overall, with an average of 1% degradation in the SSIM metrics for depth reconstruction shown in Figs. 3–11, the

Figure 13 SSIM against Resource and Power for the ADMM solver: LUT is considered as the most important resource representing logic area while the power in mW is considered. The higher the SSIM value with less LUT and Power, the better.



Deringer





(b) SSIM v.s. Power

cost of the ADMM solver using mixed precision is reduced by up to 67% for Look-Up-Tables (LUT), up to 80% for DSP units, when compared to single precision, and reduced by up to 74% for LUT and 78% for DSP for the PGD solver. The estimated dynamic power consumption using mixed precision is also significantly reduced by over 60% and 55% for the ADMM and PGD solvers respectively compared to single precision, and by approximately 10% compared to the corresponding consistent precision.

For the same sensing scenes with similar SSIM, FXP mixed precision has similar resource utilization to its comparative consistent precision, while the cost with FP mixed precision is slightly lower than its comparative consistent precision with reductions of up to 10% LUT. Accordingly, similar phenomena occur for the power consumption between FXP or FP mixed precision and their comparative

consistent precision solutions. The largest advantage of FXP mixed precision over FP occurs in the first short range scenario, which has over 50% reduction in LUT than its corresponding mixed precision accelerator with FP for both the ADMM and PGD solvers.

The bit-width of mixed precision varies between 18 to 28 bits. According to Table 1 and Figs. 3–11, compared to mixed floating-point precision, mixed fixed-point is generally more efficient in terms of power per computation, as measured by  $\mu$ J/Pixel. In contrast, mixed floating point uses less representation bits, which requires less bandwidth for data communication links. Both benefits are important depending on the design requirements.

Finally, we observe that the implemented  $\ell_1$  solvers are for processing a single block within the parallel compressive depth reconstruction framework. Hence, the performance values given in Table 1 are considered as an upper bound of overall real-time depth reconstruction, assuming all the blocks are processed in parallel.

# **5** Conclusions

In this work, a mixed precision framework using both floating and fixed point arithmetic has been introduced with a pre-computed, static schedule. It has been adopted for compressive depth reconstruction design using approximate convex optimization and linear algebra libraries. By adopting a mixed precision schedule matched to the known stand-off and dynamic range of the LiDAR-sensed scene, incremental precision scaling has been applied to the  $\ell_1$  solvers, ADMM and PGD, for depth image reconstruction *lasso* using convex optimization. The results show that iterative mixed precision in both floating point and fixed point enables similar performance of depth reconstruction compared to single float precision with significant cost and power reductions using a pipelined architecture; greater than 70% in the best cases.

By analyzing the difference between the ADMM and PGD solvers using either floating point and fixed arithmetic, more efficient depth image reconstruction is enabled with the PGD solver. The benefits of the different arithmetic types have been demonstrated and shown to vary across the different scenarios. In future work, the adaptive runtime strategies to set the mixed precision based on evaluation during the iterative schedule of convex optimization should be explored, according to diverse dynamic sensing ranges and different arithmetic types.

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Data Availability There is no public data available for this work.

# Declarations

Ethical Approval There are no human or animal related experiments conducted in this research. All the authors are agreed with this submission. This work is invited extension from previous SiPS 2021 paper entitled "Mixed Precision  $\ell$ 1 Solver for Compressive Depth Reconstruction: An ADMM Case Study". Our related previous work are in reference [13, 16, 17]. We declare that this work fulfills the standard requirement of invited extended publication with over 30% new materials than its previous conference version. All the outcomes are not published elsewhere at the same time of this submission.

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